# SENSITRON SEMICONDUCTOR

TECHNICAL DATA
DATA SHEET 1161, REV. PRELIMINARY A

# Three-Phase MOSFET BRIDGE, 100 VOLT, 50 AMP

### ELECTRICAL CHARACTERISTICS PER MOSFET DEVICE (Tj=25°C UNLESS OTHERWISE SPECIFIED)

#### **MOSFET Characteristics**

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Continuous Drain Current	$I_D$	$V_{GS}=10V$ , $T_{C}=25$ °C	-	-	50	Α
		$T_{\rm C} = 80  ^{\circ}{\rm C}$			50	
Maximum Pulsed Drain	I <sub>DM</sub>	$T_C = 25$ °C	-	-	150	Α
Current						
Drain-to-Source	$V_{(BR)DSS}$	$T_j=25$ °C, $V_{GS}=0V$ ,	100	-	-	V
Breakdown Voltage		I <sub>D</sub> =500μA				
Gate-Source Threshold	$V_{GS(th)}$	$V_{DS} = V_{GS}, T_j=25$ °C	1.0	-	3.0	V
Voltage		$I_{D} = 500 \ \mu A$				
Static Drain-to-Source On	R <sub>DS</sub>	$V_{GS} = 10 \text{ V},$	-	0.013	0.018	Ω
Resistance		$I_{D} = 40 \text{ A}$				
Drain-to-Source Leakage	I <sub>DSS</sub>	$V_{GS}=0V$ , $V_{DS}=100V$ ,	-	-	250	μΑ
Current		T <sub>i</sub> =25°C V				
Turn-on Delay	t <sub>d(on)</sub>	$I_D = 50 \text{ A}, V_{GS} = 10 \text{ V},$	-	25		ns
Rise Time	t <sub>r</sub>	T <sub>j</sub> =25°C,		150		
Turn-off Delay	t <sub>d(off)</sub>	$R_G = 5\Omega$ ,		60		
Fall Time	t <sub>f</sub>	$V_{DD} = 30V$		120		
Input Capacitance	C <sub>iss</sub>	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V},$	-	3.8	-	nF
Output Capacitance	C <sub>oss</sub>	f = 1 MHz		0.90		
Reverse Transfer	$C_{rss}$			0.30		
Capacitance						
Thermal Resistance	R <sub>thjc</sub>	-		0.7	1	°C/W
Operating and Storage	T <sub>j</sub>	-	-40	-	150	°C
Junction Temperature						

#### **Source Drain Diode Characteristics**

CHARACTERISTIC	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Forward Voltage	$V_{SD}$	$T_i=25$ °C, $I_F=30A$	-	0.80	1.1	V
Reverse Recovery Time	t <sub>rr</sub>	$T_j = 25$ °C, $I_S = 30$ A,	-	90	130	ns
		di/dt = 100 A/ μs				

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#### **Gate Driver**

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT
Supply Voltage	VCC	10	15	20	V
Input On Current	HIN, LIN	1.6	-	5	mA
Opto-Isolator Logic High Input Threshold	I <sub>th</sub>		1.6		mA
Input Reverse Breakdown Voltage	BV <sub>in</sub>	5			V
Input Forward Voltage @ I <sub>in</sub> = 5mA	V <sub>F</sub>		1.5	1.7	V
Under Voltage Lockout	VCCUV	7.0	-	9.7	V
ITRIP Threshold Voltage (1)	ITRIP <sub>th</sub>	0.4	0.49	0.58	V
Turn On Delay	t <sub>ond</sub>	-	-	1000	nsec
Turn On Rise Time	t <sub>r</sub>	-	-	200	nsec
Turn Off Delay	t <sub>offd</sub>	-	-	1300	nsec
Turn Off Fall Time	t <sub>f</sub>	-	-	200	nsec
Input-Output Isolation Voltage		1000			V

<sup>(1)</sup> Once ITRIP reaches threshold, the driver latches off. This condition can be reset by holding all three low side inputs high for more than 10  $\mu$  sec or by recycling the  $V_{cc}$  supply.

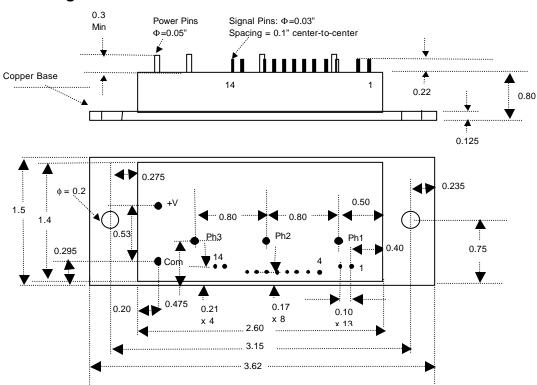
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### **Package Drawing:**



## Package Material:

Base: Copper Frame: Nickel Lid: Plastic

**Power Terminals: Copper** 

### Signal Terminals & Truth Table:

Gate Driver Truth Table					
HIN1,2,3	LIN1,2,3		HO1,2,3	LO1,2,3	
0	0		0	0	
0	1		1	0	
1	0		0	1	
1	1		0	0	

Note: This device can be used with a non-inverting input logic, if LIN and HIN are swapped.

Signal Pins		
Pin #	Function	
1	+15V	
2	PWR-GRND	
3	NC	
4	HIN1	
5	HIN2	
6	HIN3	
7	SGN-GRND	
8	LIN1	
9	LIN2	
10	LIN3	
11	SGN-GRND	
12	NC	
13	ITRIP	
14	ITRIP-RTN	

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